KL-310
Advanced Digital Logic Lab

Cover different levels of logic circuit experiments

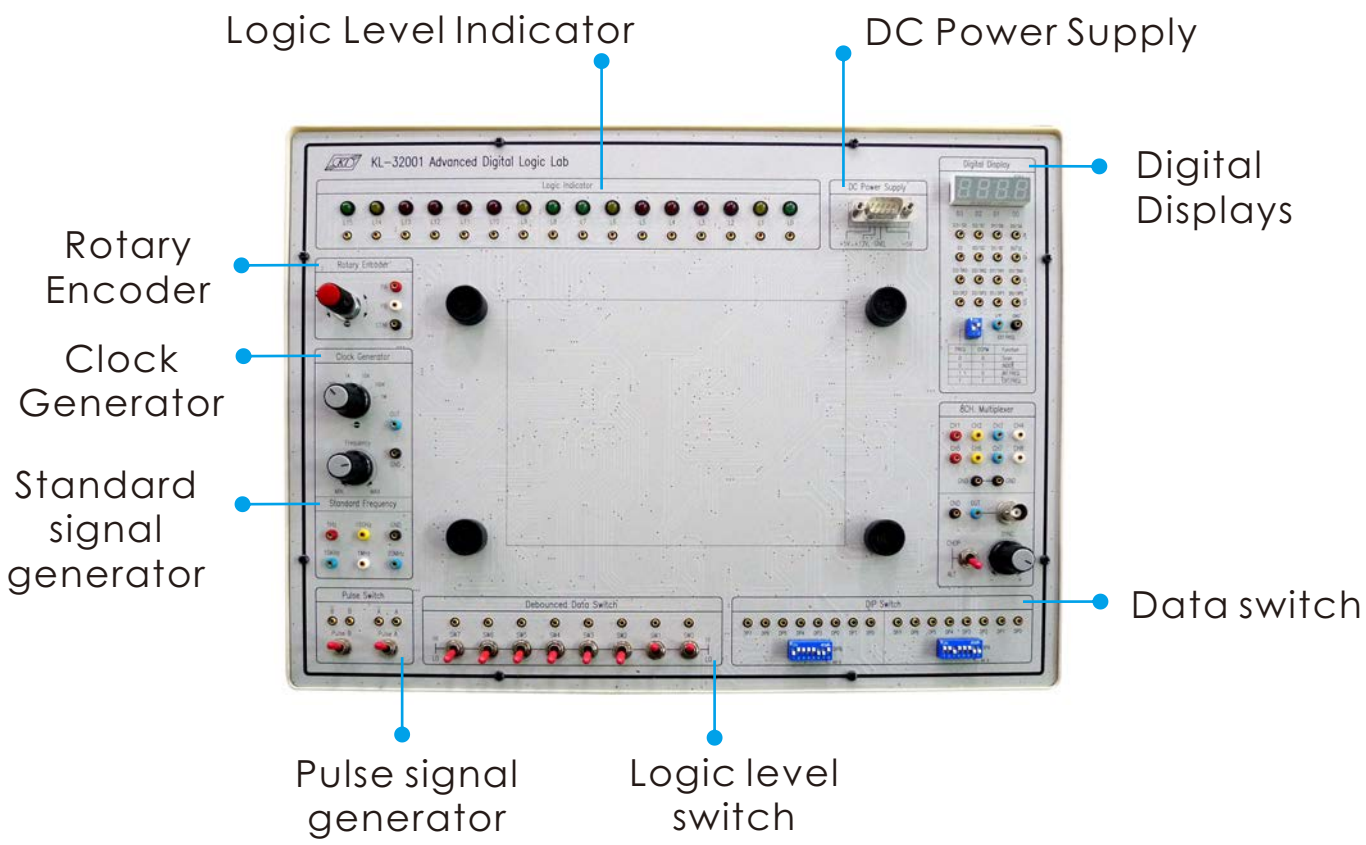
Including combinational logic, sequential logic as well as the logic circuit interfacing with microcontroller and practical application circuit for daily use.

Fully designed by FPGA/CPLD logic circuit

The system adopts FPGA/CPLD logic circuit and is equipped with snubber circuits which enhance protection for all modules, powered by main unit through power socket, avoiding wrong input power source during experiments.

Universal CPLD & breadboard

Students can implement their own circuit from universal CPLD & breadboard experiment module, making it possible to prototype most analog and digital circuits in the system.



List of Experiments

- Combinational Logic Circuit Experiment
- Arithmetical Logic / Tri-state & Code Converter Experiment
- Encoder, Decoder & Multiplexer Logic Circuit Experiment
- Flip-flop & Sequential Logic & Counter Circuit Experiment
- Oscillator/Pulser; Load Up/Down Counter Circuit Experiment
- Memory, Matrix LED & DAC/ADC & MCU Interface Circuit Experiment
- Digital & Analog Timer, Pulse Generator Circuit Experiment
- Ramp-compare/SAR/Dual-slope ADC Experiment
- Keyboard & Display for Stepping Motor Position Control
- Precise Digital Clock Timer
- Universal CPLD & Breadboard Experiment